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Docket No. SEARCHP.011C1DV1

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March 25, 2002

(Date)

Robert F. Gazdzinski
Reg. No. 39,990

Applicant : Eric M. Dowling

Serial No.: 10/074,779

Filed : February 13, 2002

For : **EMBEDDED-DRAM-DSP
ARCHITECTURE**

Examiner : Unknown

Art Unit : 2154

REQUEST FOR CORRECTION OF FILING RECEIPT

Assistant Commissioner for Patents
Washington, D.C. 20231

ATTN: APPLICATION PROCESSING DIVISION
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Dear Sir:

Applicant hereby respectfully requests that the official Filing Receipt be corrected as

follows:

The Continuing Data as Claimed by Applicant should read:

THIS APPLN IS A DIVISIONAL OF 10/001,007 filed 11/14/2001
WHICH CLAIMS PRIORITY BENEFIT OF APPLN 09/021,933 FILED 02/11/1998
WHICH CLAIMS PRIORITY BENEFIT OF APPLN 60/054,439 FILED 08/01/1997

Application No.: 10/074,779
Filed: February 13, 2002
Attorney Docket No. SEARCHP.011C1DV1

A copy of the Filing Receipt with the correction indicated in red ink is enclosed. Please forward the corrected Filing Receipt to the undersigned.

Respectfully submitted,

GAZDZINSKI & ASSOCIATES

Dated: March 25, 2002

By: 

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/074,779	02/13/2002	2154	1766	SEARCHP.011C1DV1	6	49	9

CONFIRMATION NO. 7948

FILING RECEIPT



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Suite A232
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Applicant(s)

Eric M. Dowling, Richardson, TX;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A DIV OF 10/001,007 11/14/2001 WHICH CLAIMS PRIORITY BENEFIT

OF APPLN 09/021,933 WHICH CLAIMS BENEFIT OF 60/054,439 08/01/1997
FILED 02/11/1998

Foreign Applications

If Required, Foreign Filing License Granted 02/27/2002

Projected Publication Date: 06/06/2002

Non-Publication Request: No

Early Publication Request: No

Title

Embedded-DRAM-DSP architecture

Preliminary Class

709

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Title 37, Code of Federal Regulations, 5.11 & 5.15

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